

DECLARATION OF SOLE INVENTOR FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated
below next to my name.

I believe I am the original, first and sole inventor of the subject
matter which is claimed and for which a patent is sought on the
invention entitled: Conductive Lines, Coaxial Lines, Integrated Circuitry,
and Methods of Forming Conductive Lines, Coaxial Lines, and Integrated
Circuitry, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents
of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to
be material to patentability as defined in Title 37, Code of Federal
Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's
certificates have been filed prior to the date of execution of this
declaration.

POWER OF ATTORNEY:

As a named Inventor, I hereby appoint the following attorneys and
agent to prosecute this application and transact all business in the
Patent and Trademark Office connected therewith: Richard J. St. John,

1 Reg. No. 19,363; David P. Roberts, Reg. No. 23,032; Randy A. Gregory,
2 Reg. No. 30,386; Mark S. Matkin, Reg. No. 32,268; James L. Price, Reg.
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8 Michael L. Lynch, Reg. No. 30,871.

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10 GREGORY & MATKIN P.S., 601 W. First Avenue, Suite 1300, Spokane,
11 WA 99204-0317. Direct telephone calls to: Lance R. Sadler
12 (509) 624-4276.

13 I hereby declare that all statements made herein of my own
14 knowledge are true and that all statements made on information and
15 belief are believed to be true; and further that these statements were
16 made with the knowledge that willful false statements and the like so
17 made are punishable by fine or imprisonment, or both, under

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1 Section 1001 of Title 18 of the United States Code and that such willful
2 false statement may jeopardize the validity of the application or any
3 patent issued therefrom.

4 * * * * *

5 Full name of sole inventor: Kie Y. Ahn

6 Inventor's Signature: Kie Y. Ahn

7 Date: Aug 19, 1997

8 Residence: Chappaqua, New York

9 Citizenship: U.S.A.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 08/917,003
Priority Filing Date August 20, 1997
Inventor Kie Y. Ahn
Assignee Micron Technology, Inc.
Priority Group Art Unit 2823
Priority Examiner K. Eaton
Attorney's Docket No. MI22-1738
Title: Conductive Lines, Coaxial Lines, Integrated Circuitry, and Methods of
Forming Conductive Lines, Coaxial Lines, and Integrated Circuitry

Assistant Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Please recognize Frederick M. Fliegel, Reg. No. 36,138; D. Brent Kenady, Reg. No. 40,045; James E. Lake, Reg. No. 44,854; Bernard Berman, Reg. No. 37,279; and Jennifer Taylor, Reg. No. P-48,711; whose post office address is 601 W. First Avenue, Suite 1300, Spokane, Washington 99201-3828, as associate attorneys or agents in the above-entitled application.

Date: 6-20-01



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